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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/458,370	12/09/1999	LOUIS A. LIPPINCOTT	10559/105001	8772
20985 7	7590 07/28/2003			
FISH & RICHARDSON, PC			EXAMINER	
4350 LA JOLLA VILLAGE DRIVE SUITE 500			HESSELTINE, RYAN J	
	SAN DIEGO, CA 92122		<u></u>	<del></del>
	··· /2.22		ART UNIT	PAPER NUMBER
			2623	
		,	DATE MAILED: 07/28/2003	
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Please find below and/or attached an Office communication concerning this application or proceeding.

1,.	Application No.	Applicant(s)			
Advisory Action	09/458,370	LIPPINCOTT, LOUIS A.			
· · · · · · · · · · · · · · · · · · ·	Examiner	Art Unit			
	Ryan J Hesseltine	2623			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address					
THE REPLY FILED 16 July 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.					
PERIOD FOR REPLY [check either a) or b)]					
a) The period for reply expiresmonths from the mailing date of the final rejection.  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).  Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
1. A Notice of Appeal was filed on Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.					
2. The proposed amendment(s) will not be entered because:					
(a) ☑ they raise new issues that would require further consideration and/or search (see NOTE below);					
(b) ☐ they raise the issue of new matter (see Note below);					
(c) \(  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or					
(d) they present additional claims without canceling a corresponding number of finally rejected claims.					
NOTE: See Continuation Sheet.					
3. Applicant's reply has overcome the following rejection(s):					
Newly proposed or amended claim(s) would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).					
5. ☑ The a) ☐ affidavit, b) ☐ exhibit, or c) ☑ request fo application in condition for allowance because: See	The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.				
6. The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.				
	For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.				
The status of the claim(s) is (or will be) as follows:					
Claim(s) allowed:					
Claim(s) objected to:					
Claim(s) rejected: <u>1-17,19-21 and 23-27</u> .					
Claim(s) withdrawn from consideration:					
☐ The proposed drawing correction filed on <u>11 March 2003</u> is a) ☐ approved or b) ☐ disapproved by the Examiner.					
Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s)					
10. Other:					





Continuation of 2. NOTE: Amendments to the claims raise 35 USC 112, 2nd paragraph issues such as in line 5-6 of claim 1 which states in part "operate on a matrix of coefficients in concurrently two same directions..." and in line 7-8 of claim 23 which states in part "each state indicating the direction of operation of both each one-dimensional inverse..." which does not correspond to how claim 25 was amended at line 7-8 which states in part "each state indicating the direction of operation of both one-dimensional inverse..." The proposed amendments also add new limitations to the claims not previously considered such as concurrently operating on a matrix of coefficients in the same direction as recited in claims 1 and 8 and executing an additional step in each of claims 15 and 19.

Continuation of 5. does NOT place the application in condition for allowance because: In light of applicant's amendments, the examiner believes that all claimed limitations are still met by USPN 5,268,853 to Tanaka et al. The reference teaches the use of a memory device for temporarily storing the calculated results of a first one-dimensional DCT calculator wherein data are written to the memory device in a row direction and read out in a column direction, after which, new data are written to the memory device in column direction and are read out in row direction, further, new data are written to the memory device in row direction (column 15, line 10-28). Tanaka further adds that it is possible to operate the orthogonal transformation porcessor at high speed by simultaneously performing a writing operation with respect to one memory unit and a reading operation with respect to the other memory unit (column 15, line 29-34).

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 4600